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** CONTINUING DATA *****

None JKT

** FOREIGN APPLICATIONS *****

None JKT

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| 35 USC 119 (a-d) conditions met | <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance | CA | DRAWING 6 | CLAIMS 24 | CLAIMS 3 |
| Verified and Acknowledged <i>Shivashankar J. K. T.</i> Examiner's Signature | Initials | | | | |

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TITLE

Computer system having a single processor equipped to serve as multiple logical processors for pre-boot software to execute pre-boot tasks in parallel

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